

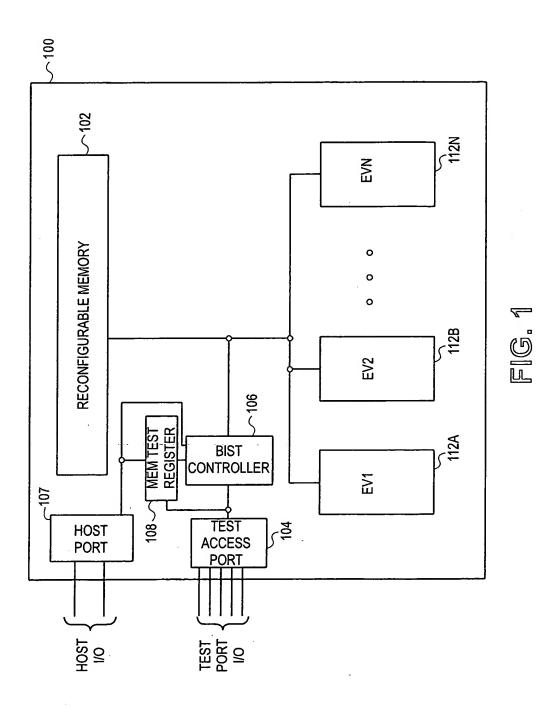
Method and Apparatus for Reconfigurable Memory
Inventor: Venkatraman, et al Filing Date: Sep

Filing Date: September 1, 2005

Attorney Docket No.: 42P14044 Application No.: 10/056,393

REPLACEMENT DRAWING

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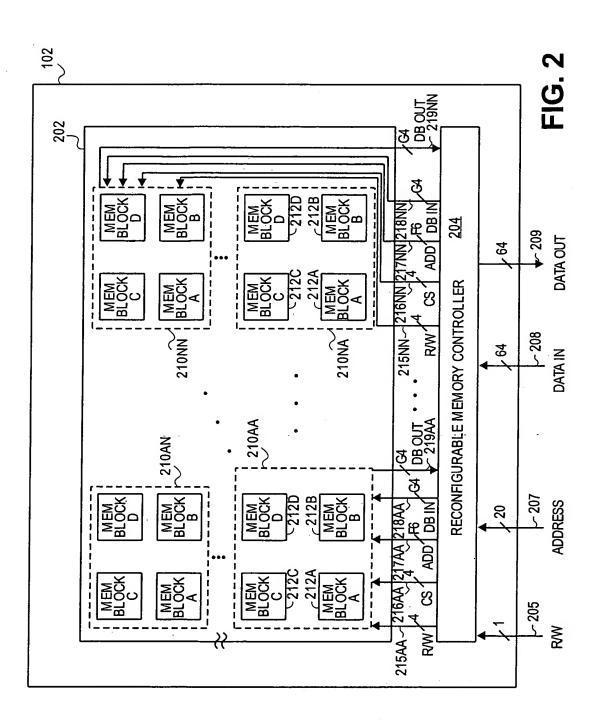
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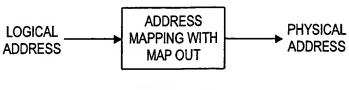


FIG. 3

LOGICAL ADDRESS W MAX/8-MOA=MAX/8 MAX/8-128K MAX/8-192K		LOGICAL BITS MAX-512K MAX-1024K MAX-1536K	MEM BLOCK CN	PHYSICAL BITS MAX MAX-512K MAX-1024K	
MAX/8-256K	\longleftrightarrow	MAX-2048K	MEM BLOCK BN MEM BLOCK AN	MAX-1536K ←	—→ MAX/8-192K —→ MAX/8-256K
MAX/8-320K	←	MAX-2560K	WEW BEOOKAN	WAX-2040N ←	→ IVIAWO-250K
		•			
•		•	•	•	•
448K		3584K	MEM BLOCK D ₂	4096K ←	→ 512K
384K	←─→	3072K	<u>-</u>	3584K ←—	→ 448K
320K	←→	2560K -	MEM BLOCK C ₂	3072K ←	→ 384K
256K	· · ·	2048K	MEM BLOCK B ₂	2560K ←	→ 320K
192K	←	1536K	MEM BLOCK A ₂	2048K ←	→ 256K
(192K-1)	←→	(1536K-1)	WEW BLOCK DA	(2048K-1) ← 1536K ←	→ (256K-1) → 192K
128K	\leftarrow	1024K	MEM BLOCK C ₁	1024K ←	→ 128K
			MEM BLOCK B ₁	<u> </u>	
64K		512K	MEM BLOCK A ₁	512K ← 212B	→ 64K
0K	←	0K		0K ←	→ 0K

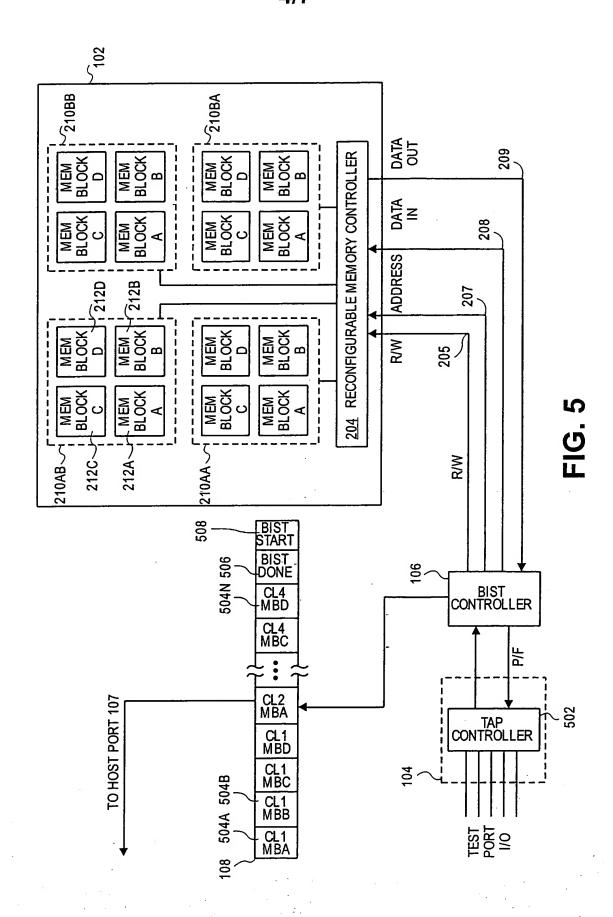
FIG. 4

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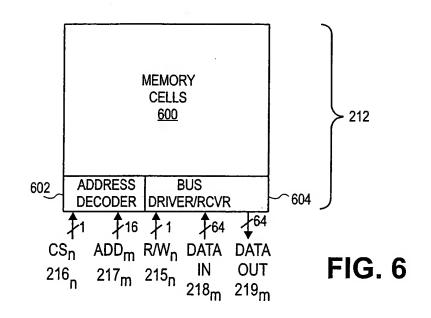


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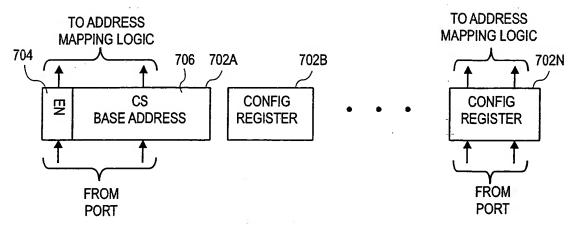


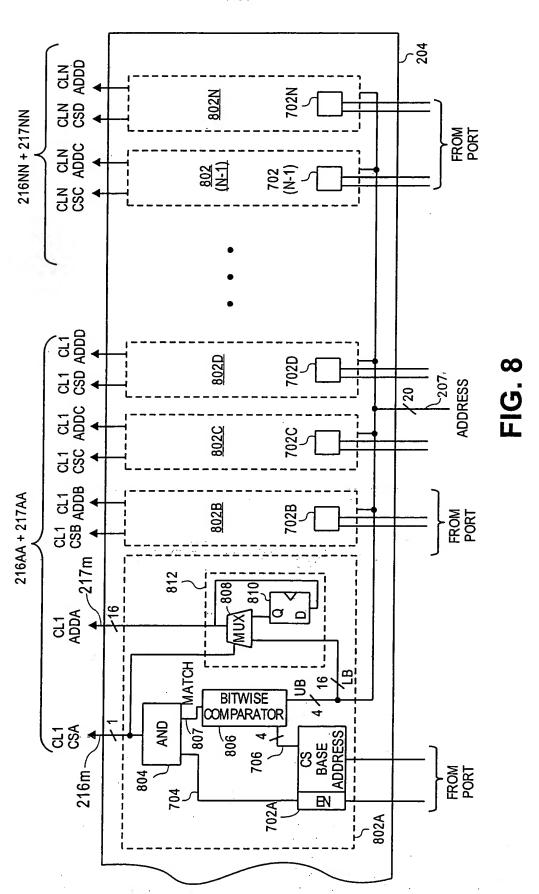
FIG. 7

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